


8/24/2020

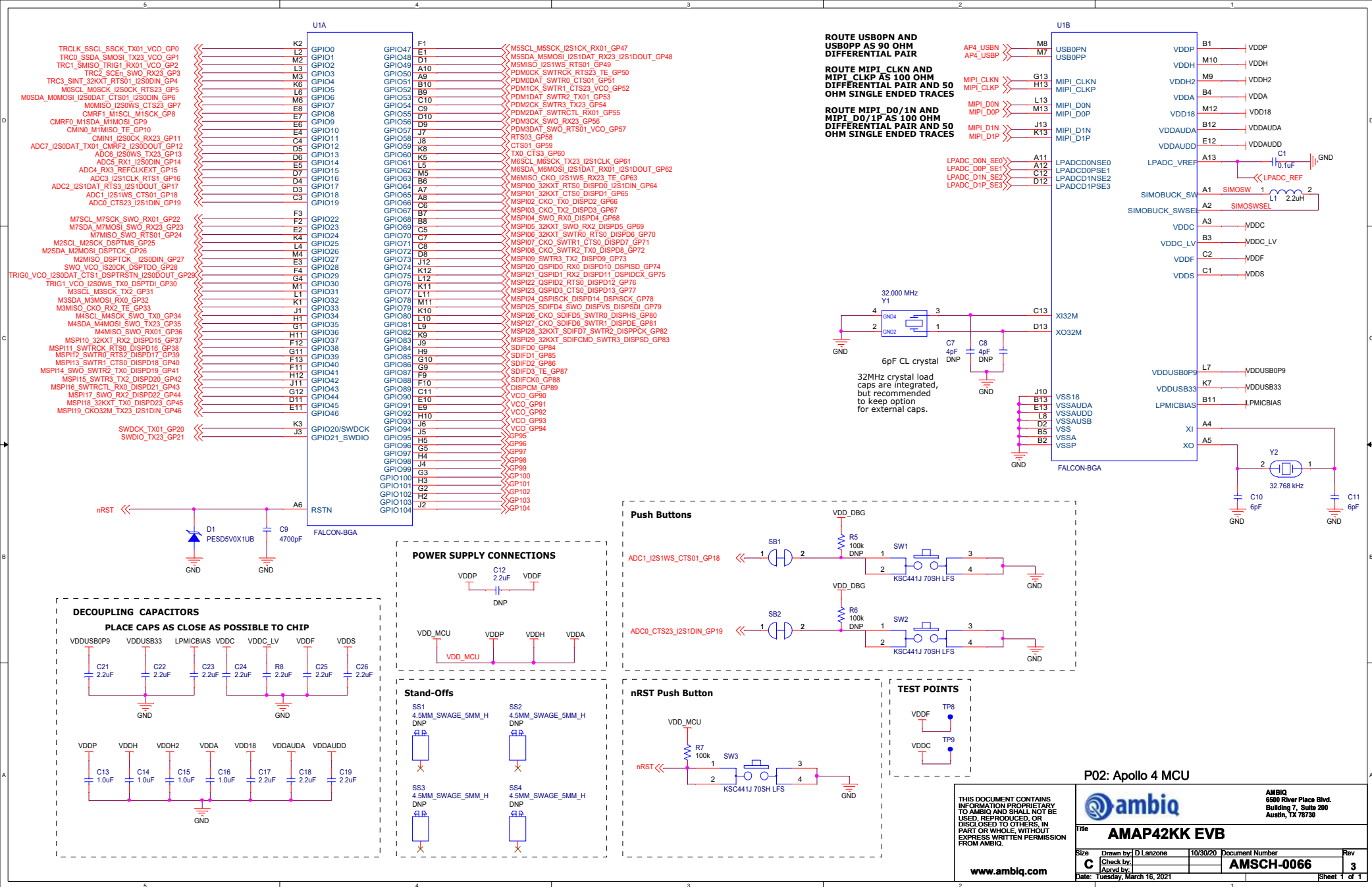
1) Updated U10-U12 to correct part number

10/16/2020

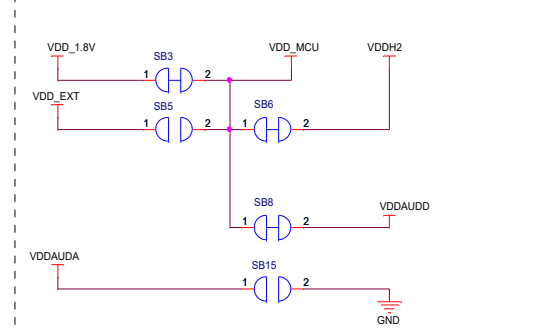
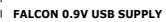
- 1) Updated D5 LED to be controlled by GP97
- 2) Added loadswitch control for USB and VDD18 power supplies, removed ground SB option to these rails (handled through loadswitches)
- 3) Added IOM4 to J9, swapped GPI01, 103, and 104 to loadswitch control
- 4) Removed J16, expanded J17 to bring LPADC out to headers, added stereo/pseudo diff options to jack inputs

P01: Revision History

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	Title <b>AMAP42KK EVB</b>				
	Size <b>C</b>	Drawn by: D Lanzone	10/30/20	Document Number <b>AMSCH-0066</b>	Rev <b>3</b>
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1.9V Default. Connect SB51 for 1.8V



### LED GPIO Solder-Bridges

The diagrams illustrate three different LED GPIO solder-bridges:

- TRIG1\_VCO\_I2S0WS\_TX0\_DSPTDI\_GP30:** This circuit uses a transistor Q3 (MMBT3904LT1G) to drive LED D3 (LED\_BLUE). The base of Q3 is connected to VDD\_MCU\_TRACK through resistor R65 (10.0k). The emitter is connected to ground. The collector is connected to AT\_3.3V through resistor R34 (10.0k). A signal source (TRIG1\_VCO\_I2S0WS\_TX0\_DSPTDI\_GP30) is connected to the base of Q3 through a buffer SB37 and resistor R66 (10.0k).
- VCO\_GP90:** This circuit uses a transistor Q4 (MMBT3904LT1G) to drive LED D4 (LED\_BLUE). The base of Q4 is connected to VDD\_MCU\_TRACK through resistor R67 (10.0k). The emitter is connected to ground. The collector is connected to AT\_3.3V through resistor R35 (10.0k). A signal source (VCO\_GP90) is connected to the base of Q4 through a buffer SB38 and resistor R68 (10.0k).
- GP97:** This circuit uses a transistor Q5 (MMBT3904LT1G) to drive LED D5 (LED\_BLUE). The base of Q5 is connected to VDD\_MCU\_TRACK through resistor R69 (10.0k). The emitter is connected to ground. The collector is connected to AT\_3.3V through resistor R36 (10.0k). A signal source (GP97) is connected to the base of Q5 through a buffer SB39 and resistor R70 (10.0k).

### P03: Power Supplies

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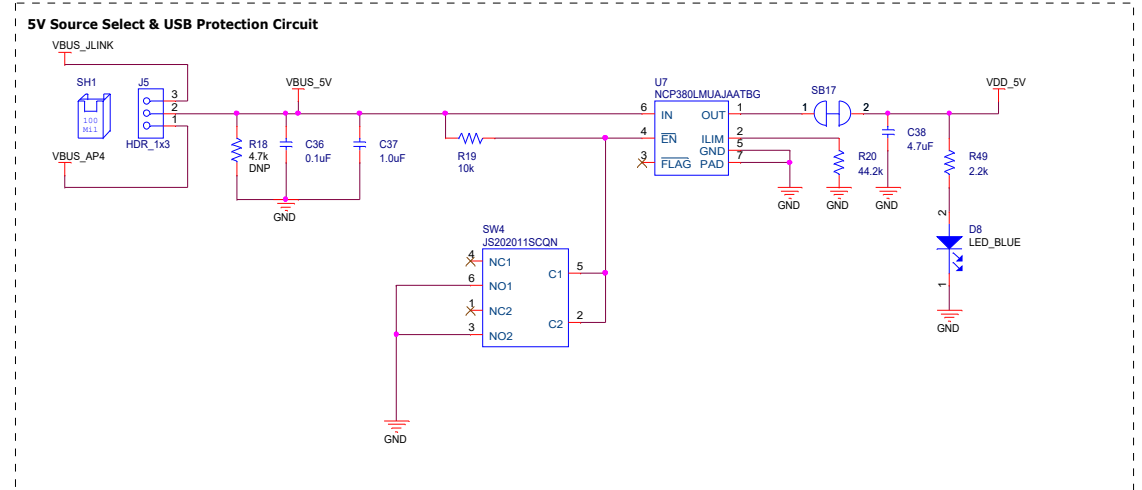
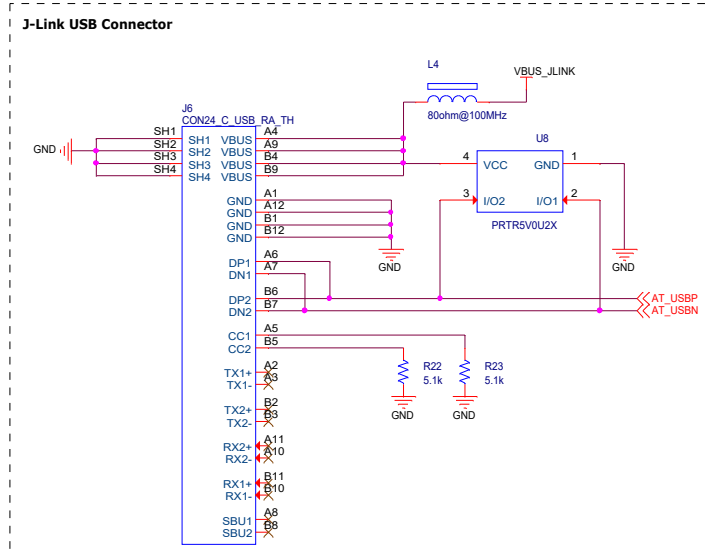
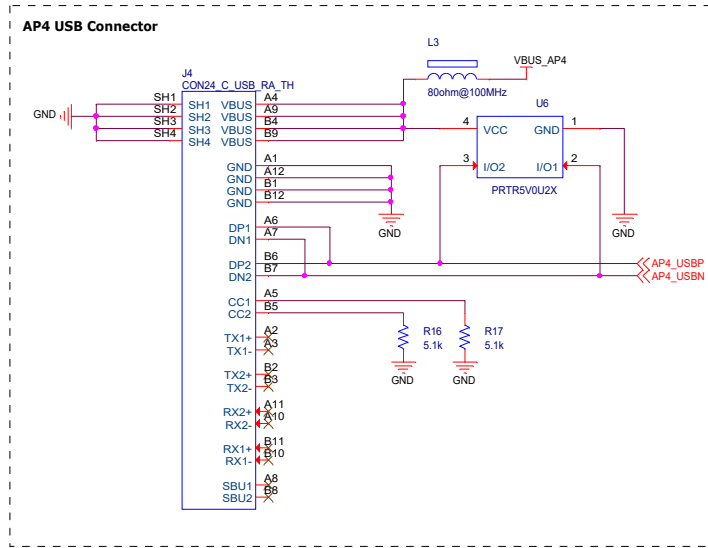
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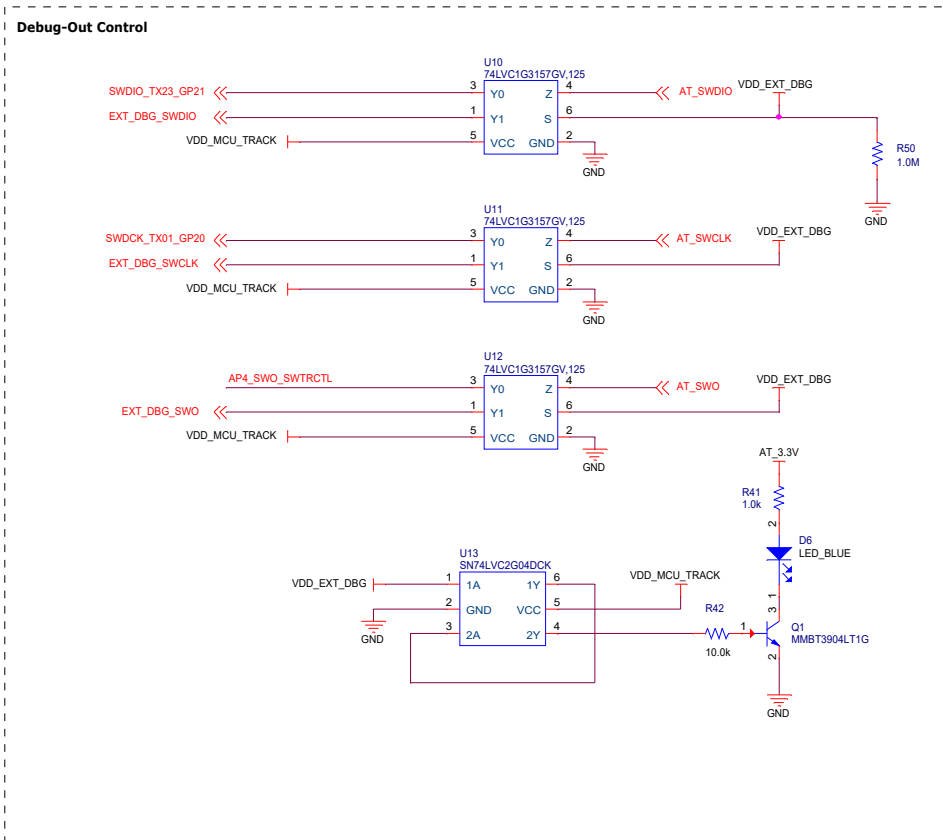
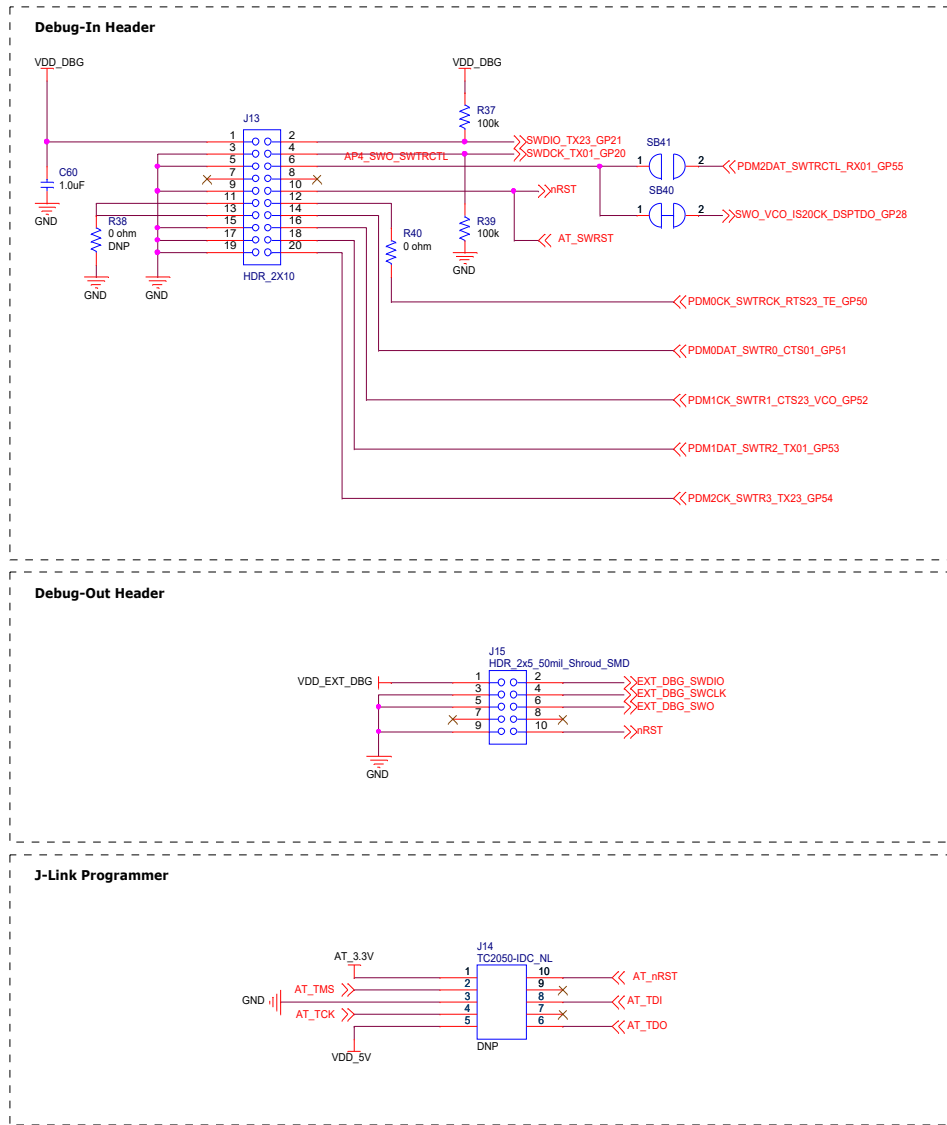
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## P07: Debug Headers and Support

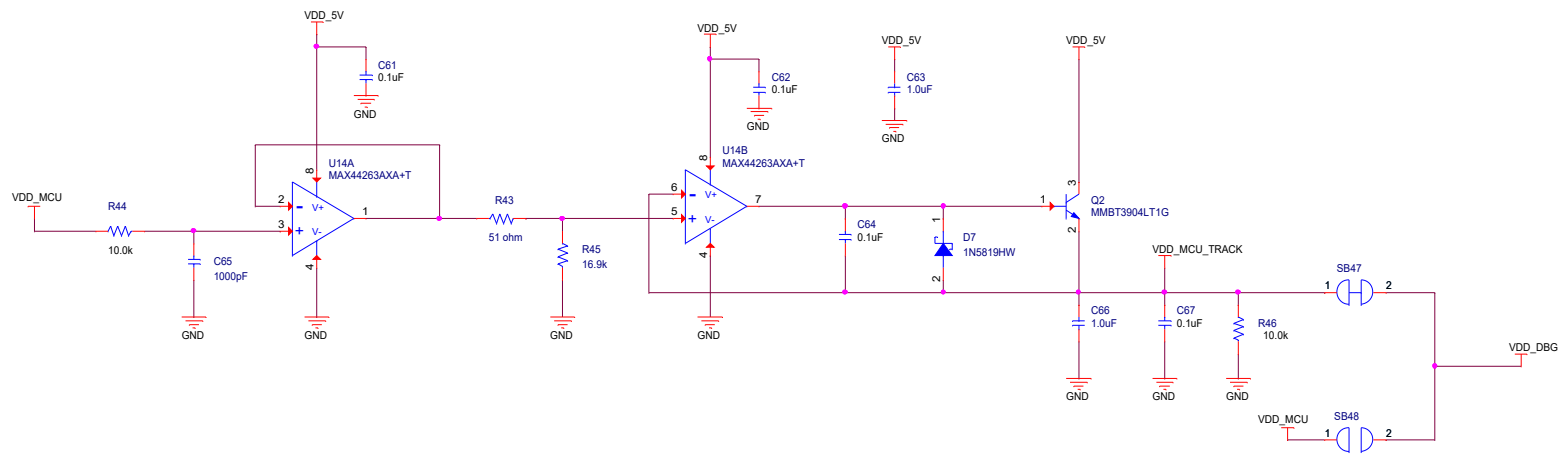
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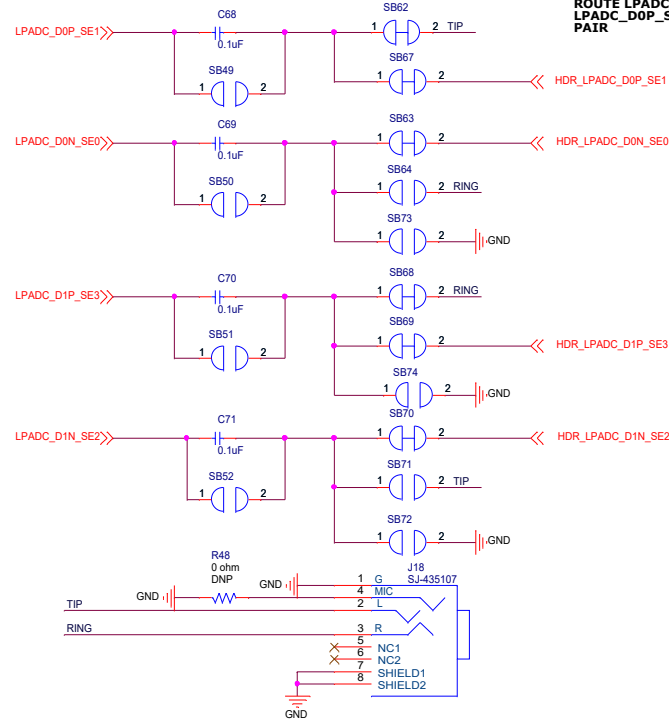


P08: Power Supply Tracker

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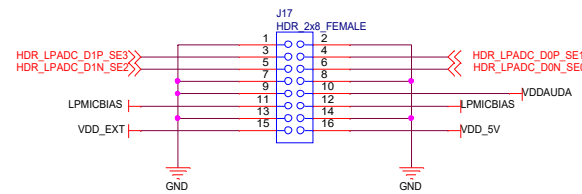


# LPADC 3.5MM Jack



ROUTE LPADC\_D1N\_SE2 AND  
LPADC\_D1P\_SE3 AS DIFFERENTIAL  
PAIR  
ROUTE LPADC\_D0N\_SE0 AND  
LPADC\_D0P\_SE1 AS DIFFERENTIAL  
PAIR

# LPADC Voltage Header



# P09: Audio Connections

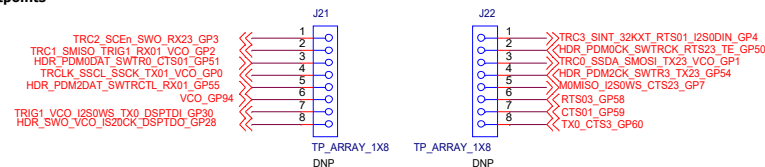
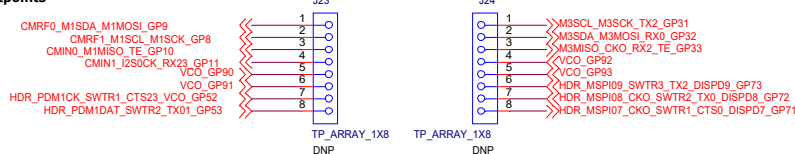
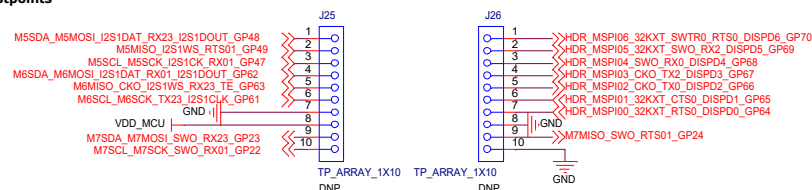
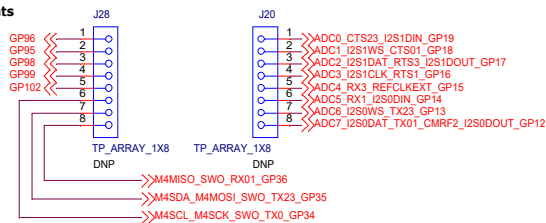
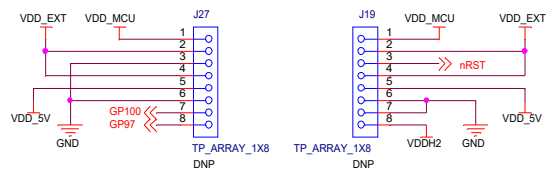
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## P10: Testpoint Arrays

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